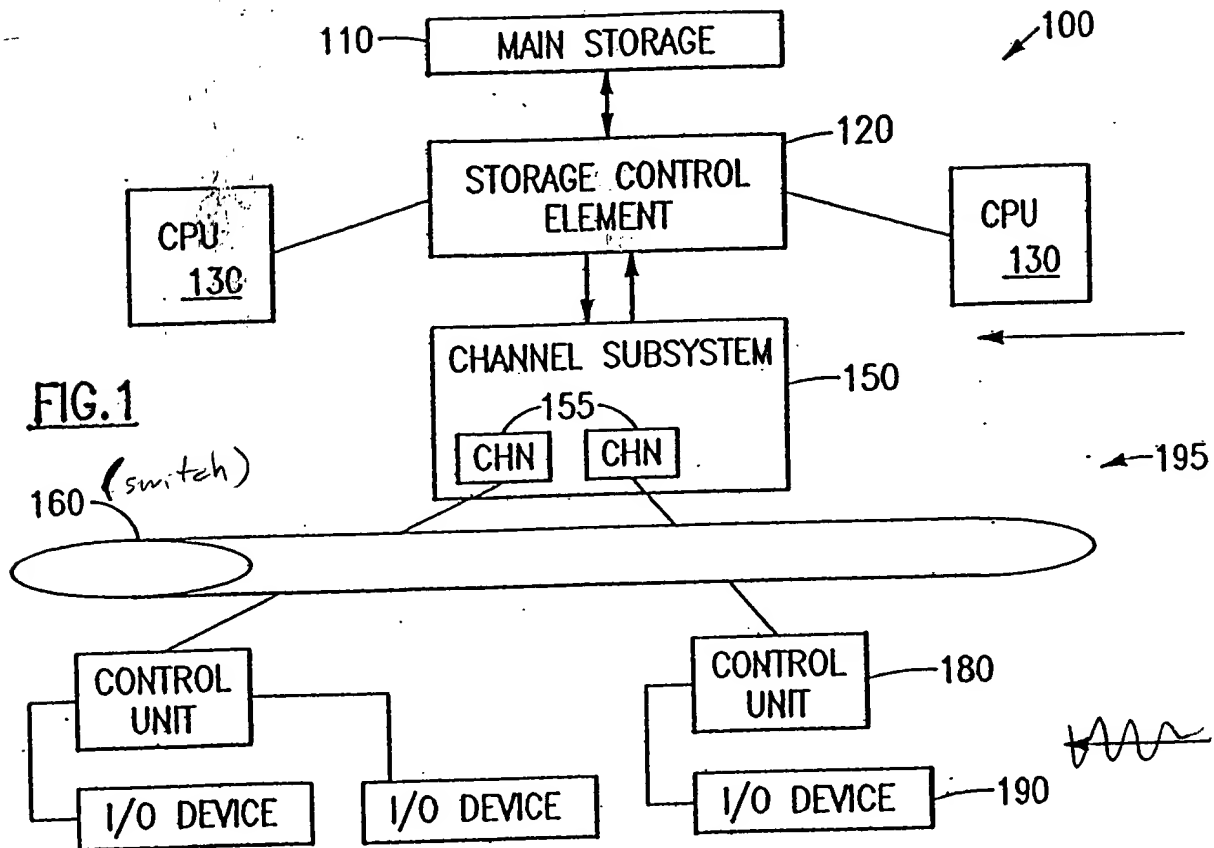


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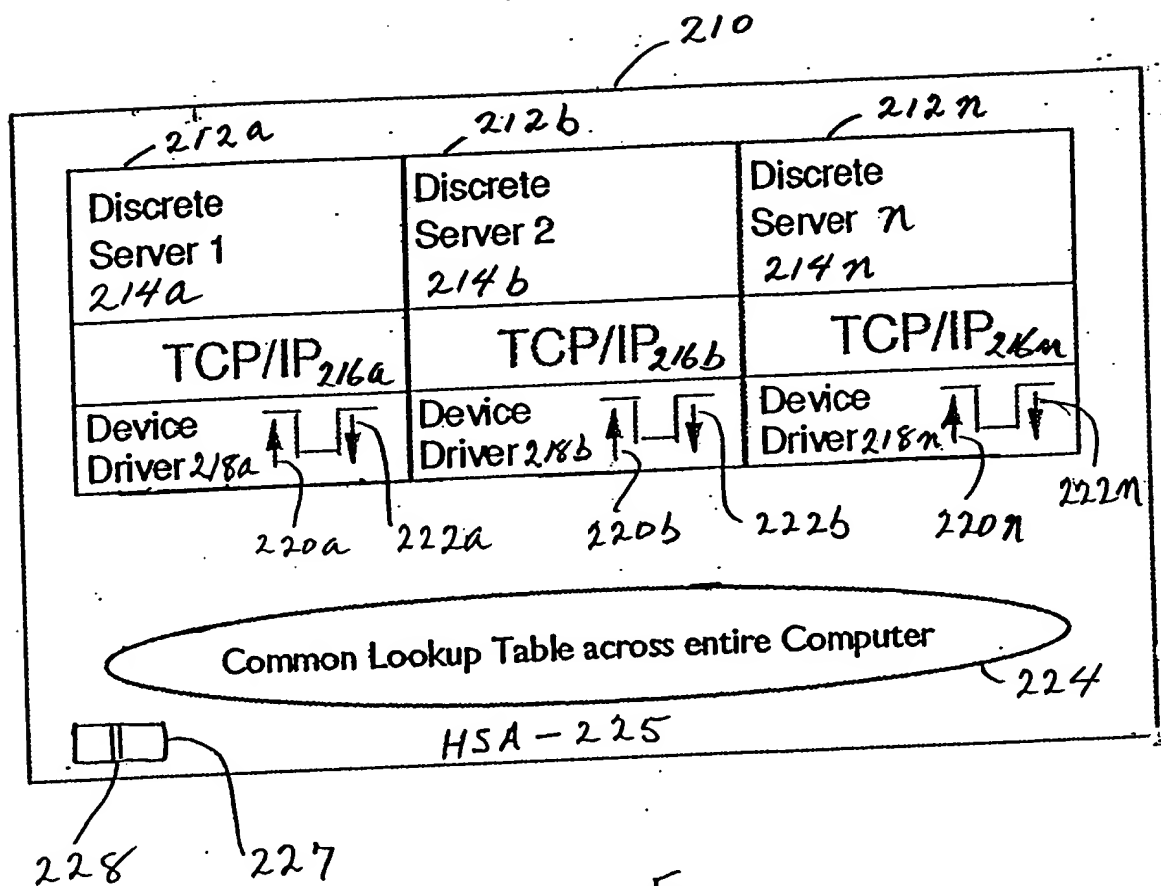
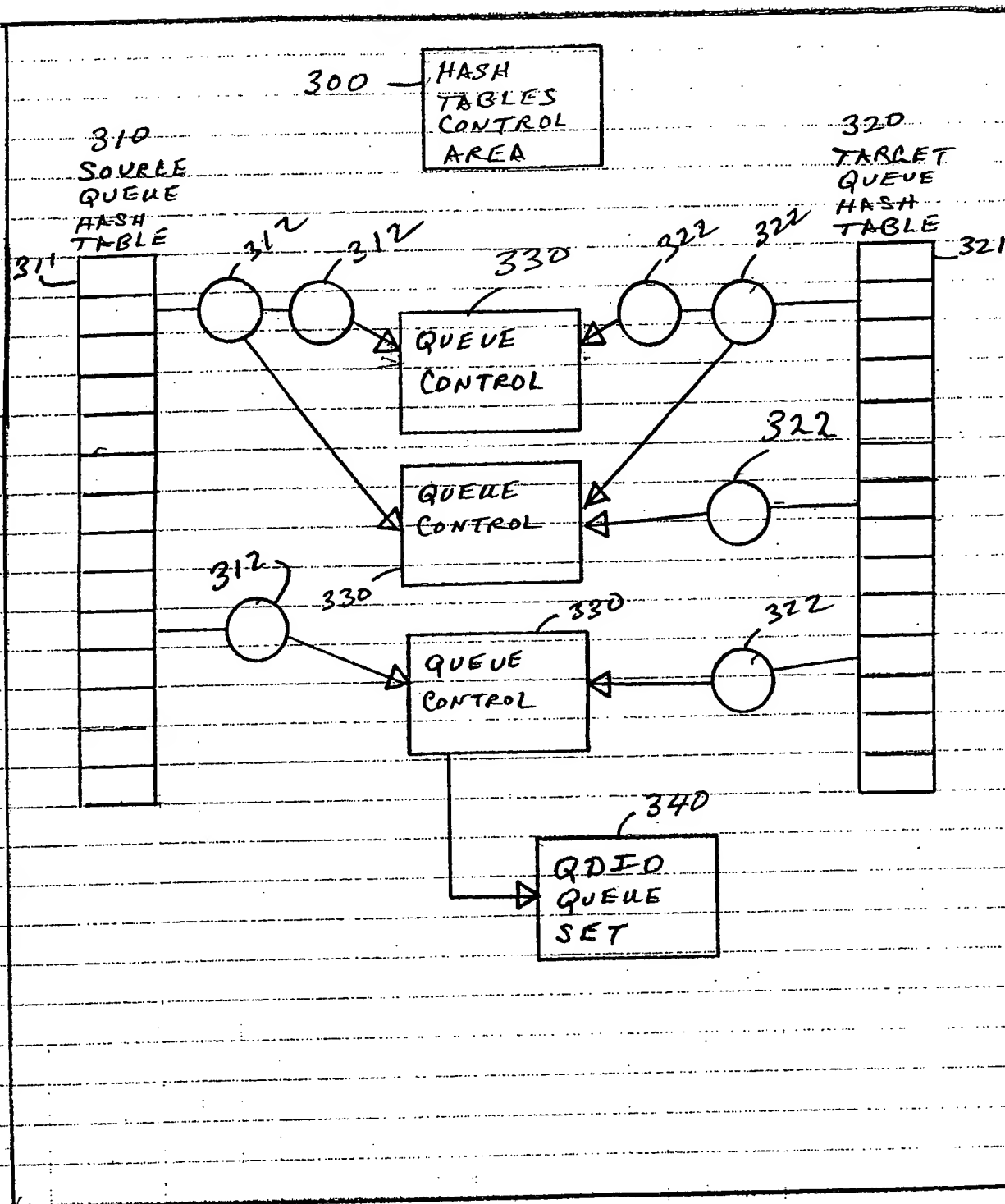


FIG 2



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FIG. 3

300- HASH TABLE CONTROL

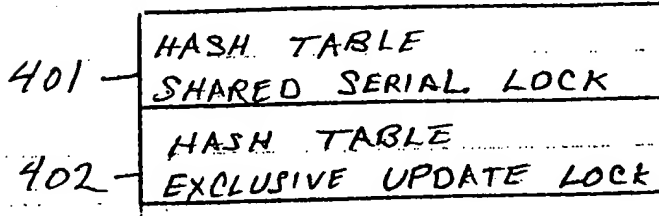


FIG. 4

330- QUEUE CONTROL

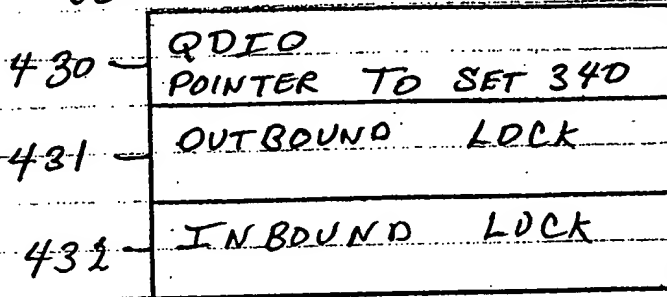
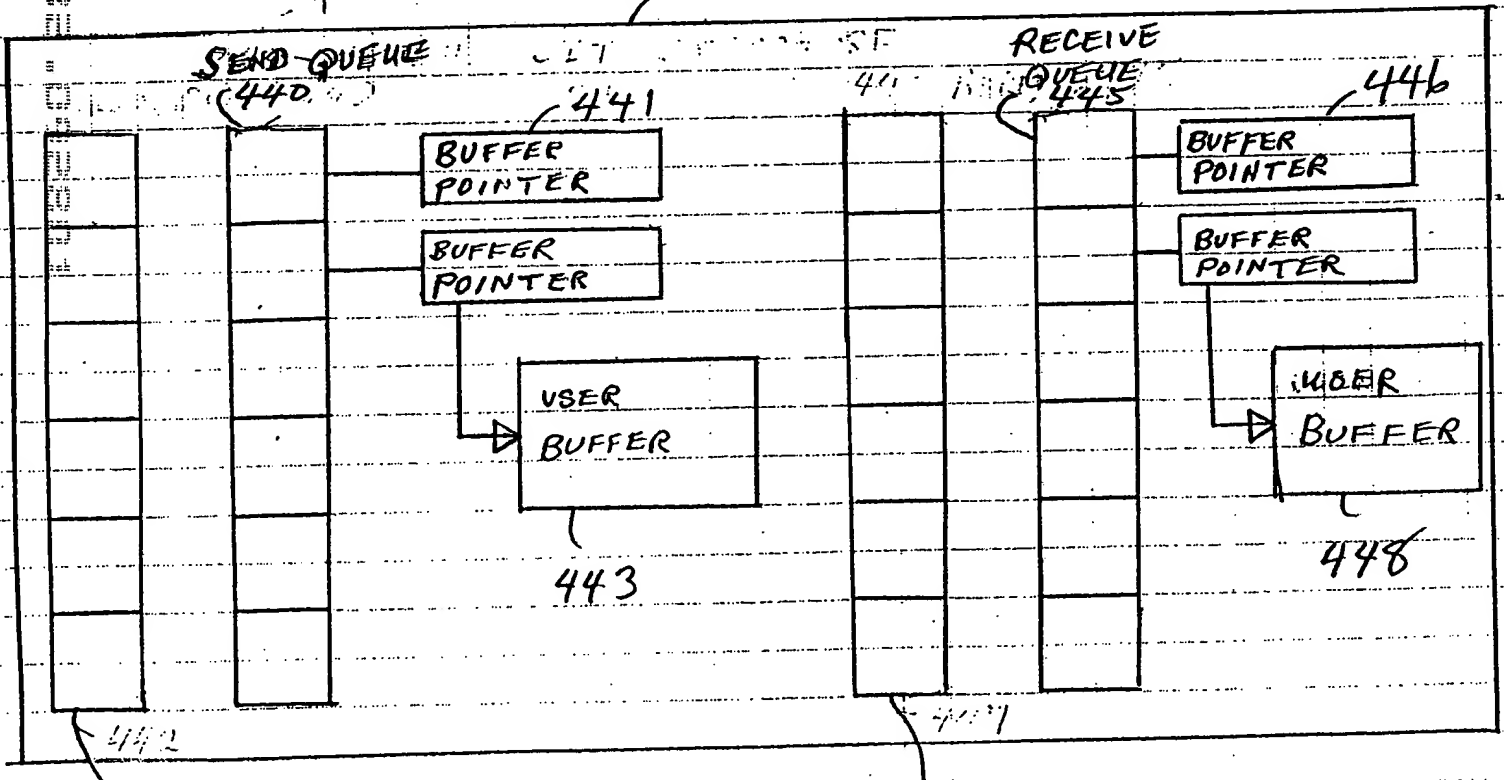


FIG. 5

QUEUE SET 340



STORAGE
LIST
STATUS
BLOCK
442

FIG. 6

STORAGE
LIST
STATUS
BLOCK
447

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243 USER BUFFER

244 TARGET IP ADDRESS

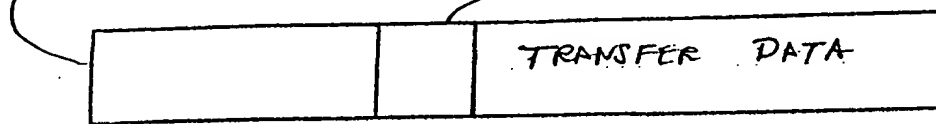


FIG. 7

312 SOURCE HASH DUPLICATE LIST ENTRIES

410-	LPAR-IP.SUBCHANNEL#
411-	STATUS
412-	POINTER TO QUEUE CONTROL 330
413-	NEXT POINTER TO 312
414-	PREVIOUS POINTER TO 311 OR 312

FIG. 8

322 TARGET HASH DUPLICATE LIST ENTRIES

420-	IP ADDRESS
421-	STATUS
422-	POINTER TO QUEUE CONTROL 330
423-	NEXT POINTER TO 322
424-	PREVIOUS POINTER TO 321 OR 322

FIG. 9

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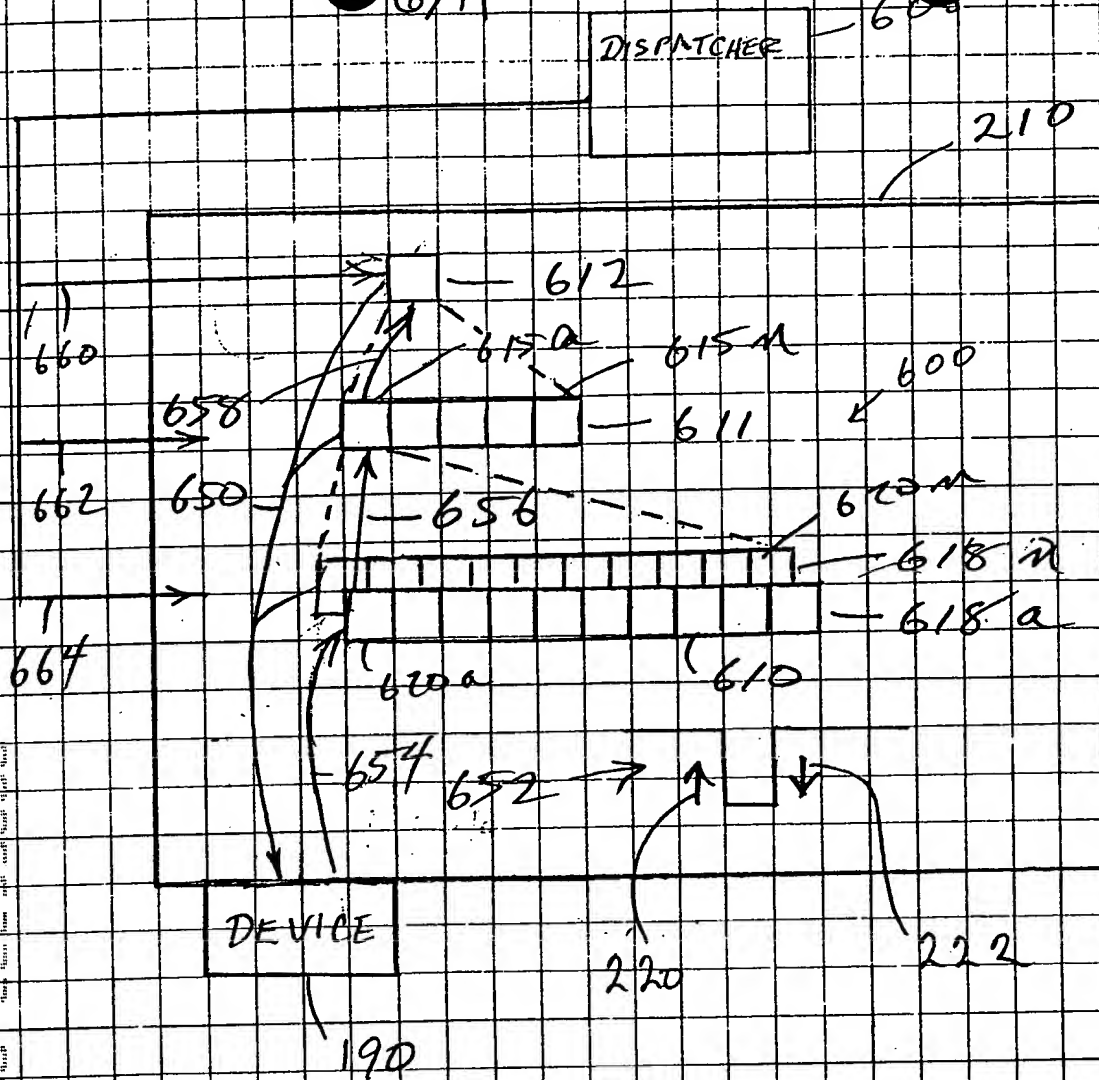


FIG. 10

POU920010063US1

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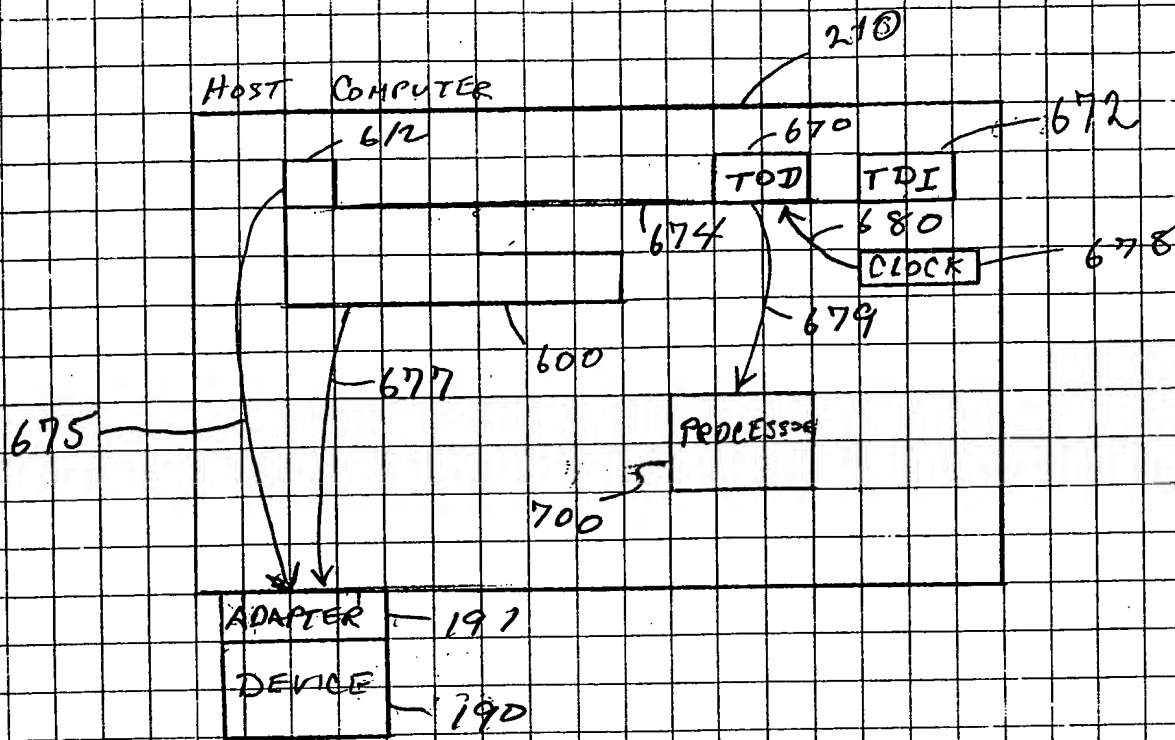


FIG. 11

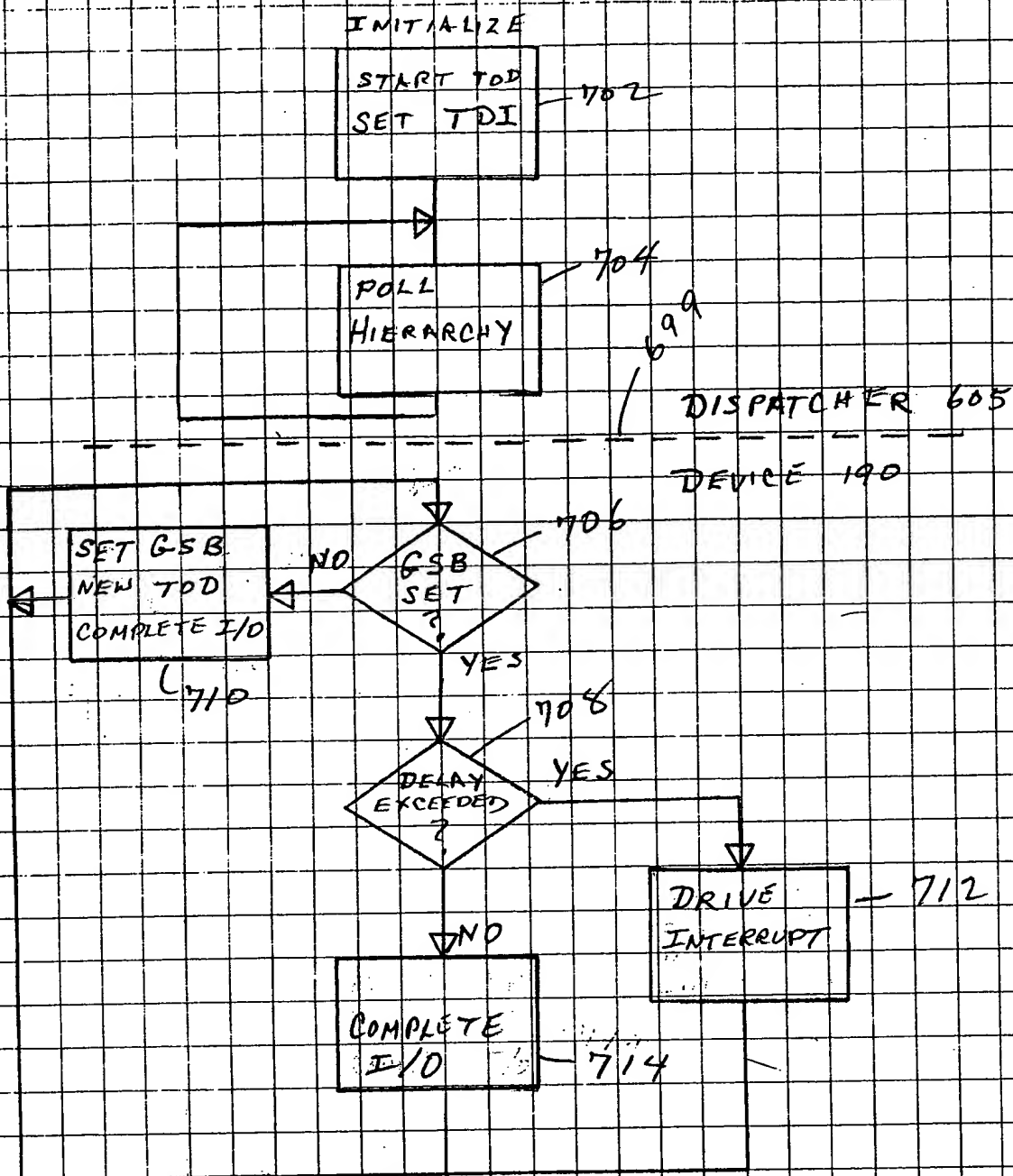


FIG 12

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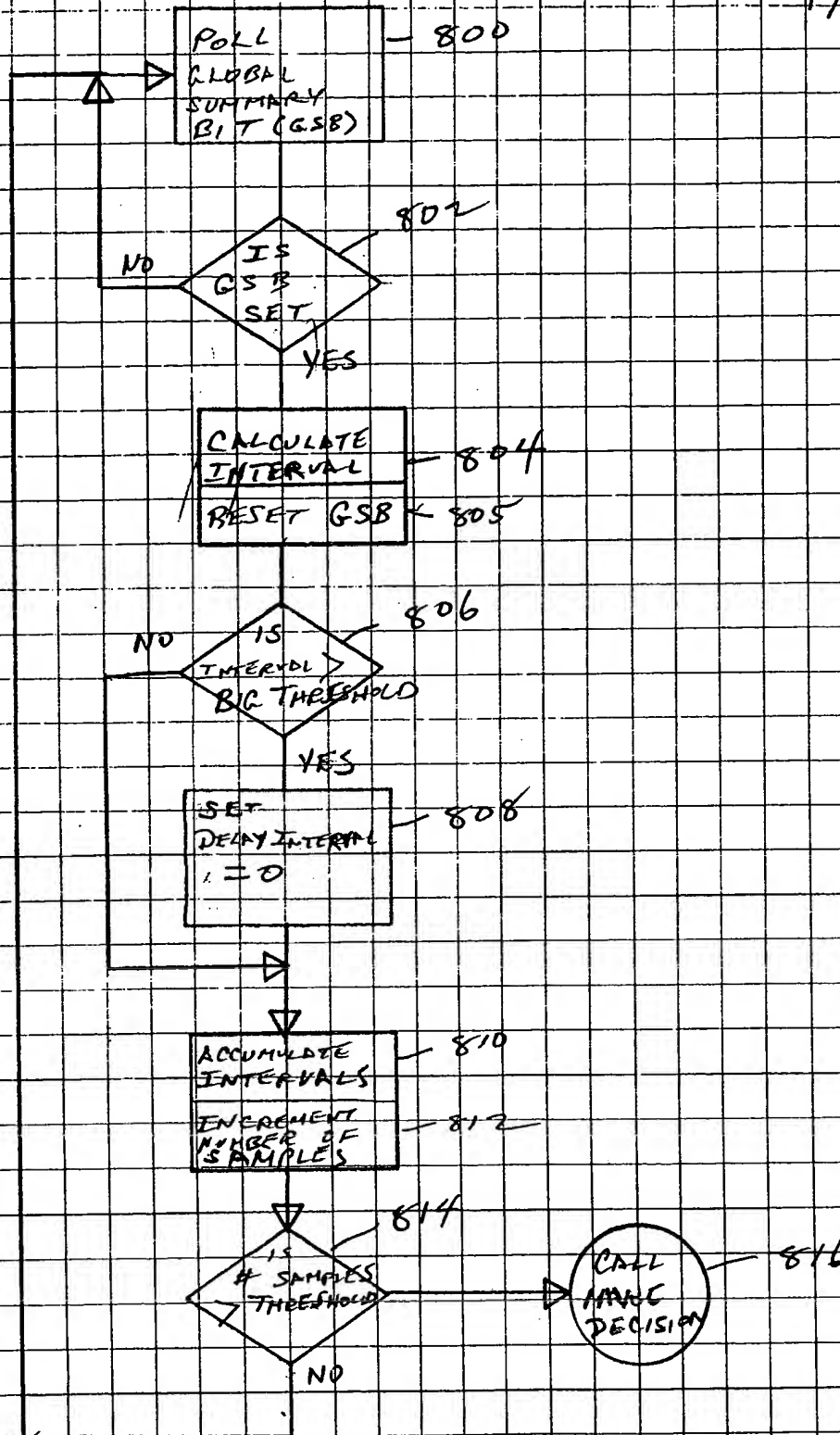


FIG 13

MAKE DECISION

POU920010063US1

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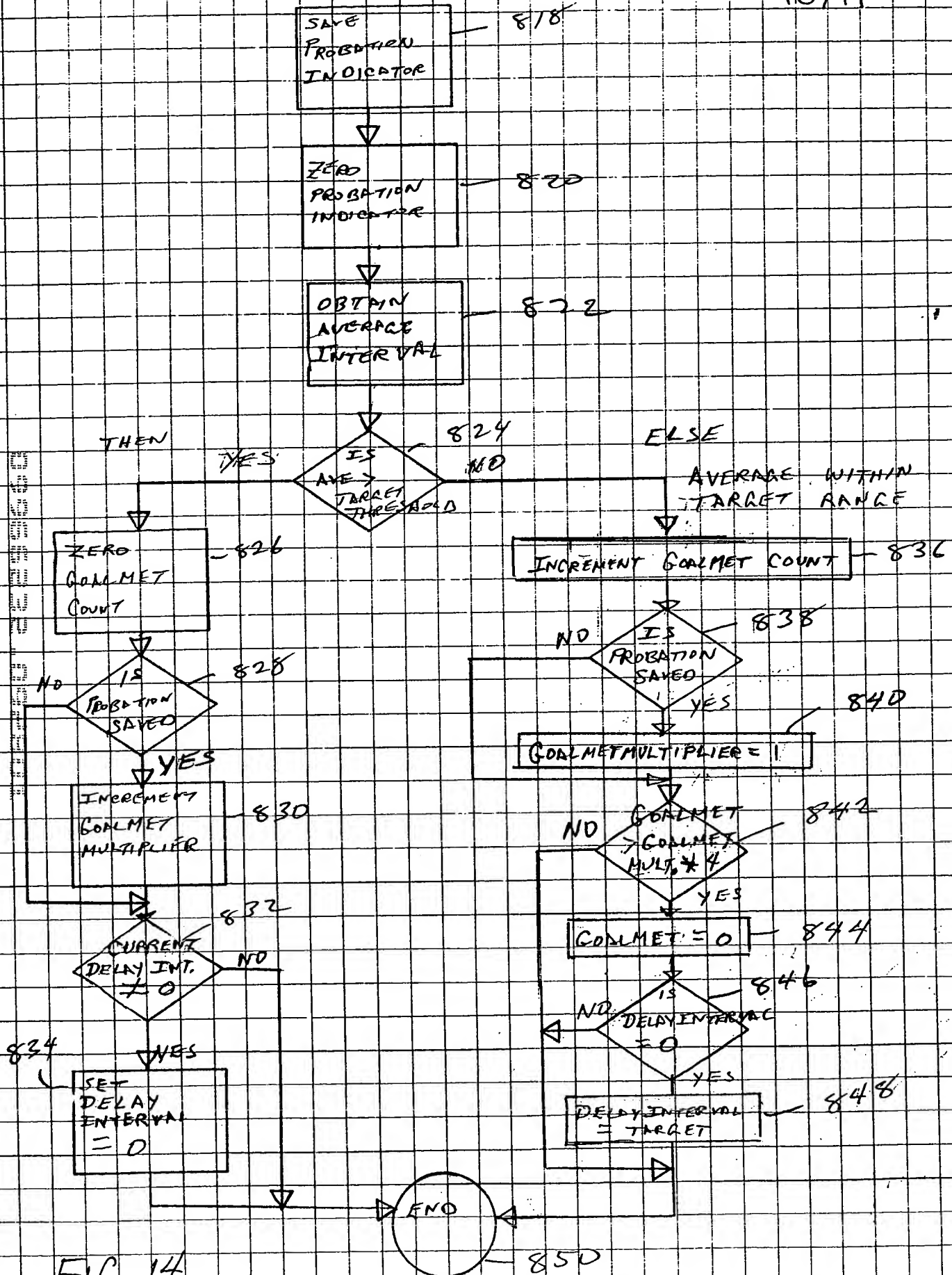


FIG 14

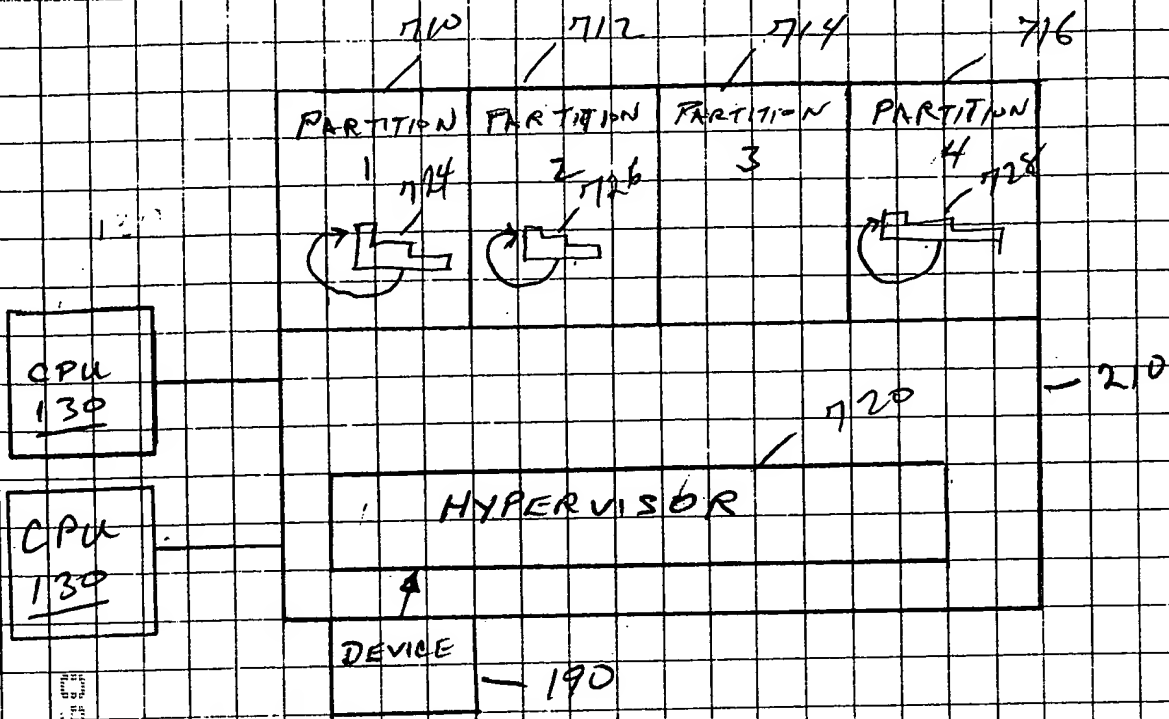


FIG 15

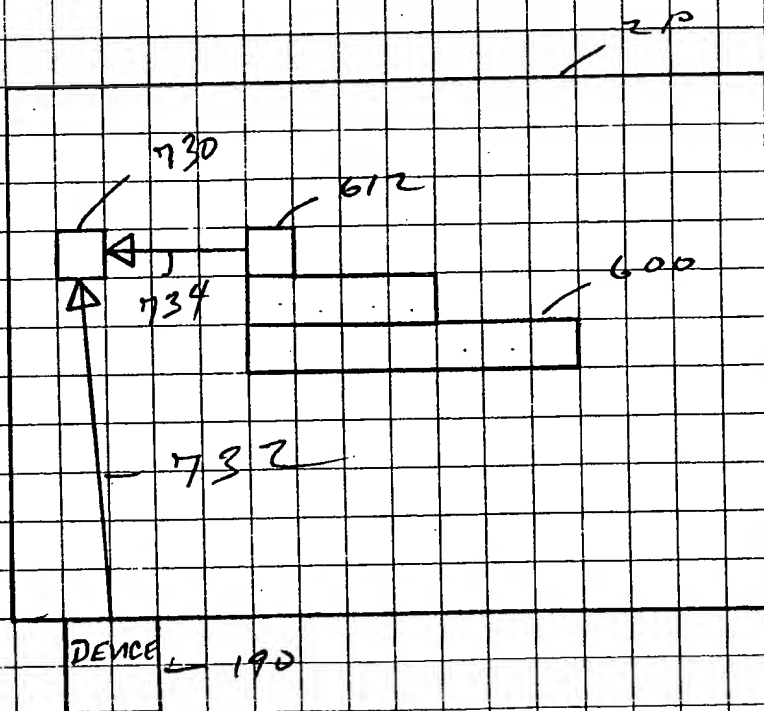


FIG 16